

FSM Lab

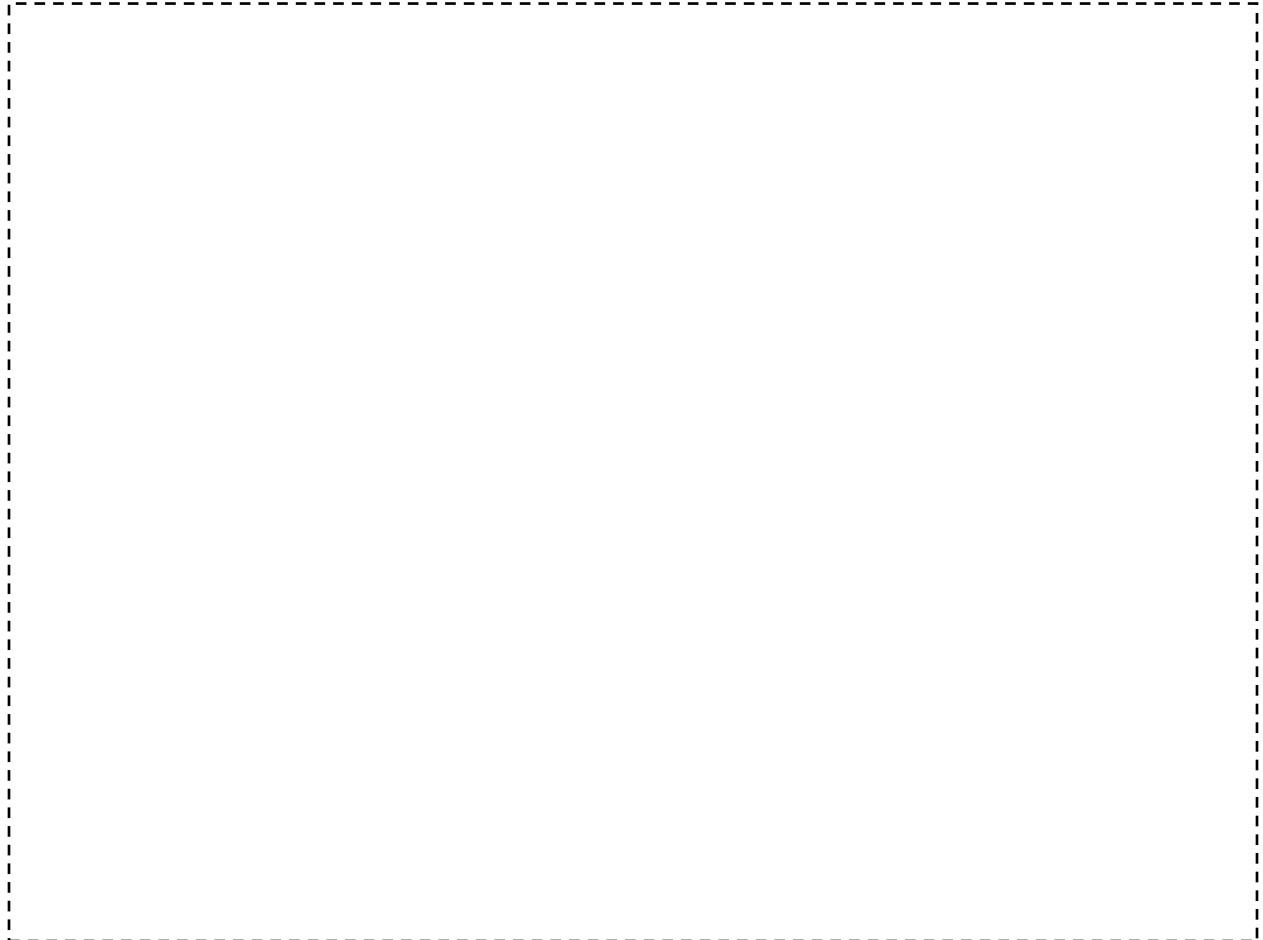
Name _____

Pre-Lab:

- Review the slides over Finite State Machines
- Complete the handout over FSMs, constructing Boolean expressions for the transition function.

Deliverables: Logical diagram for your implementation and functional HDL code.

Circuit Implementation



HDL Code

```
CHIP VENDING {
```

```
  IN _____;
```

```
  OUT _____;
```

```
  PARTS:
```

```
}
```